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Angela M. Beddawi

Applicant : Klaas Bult et al.
Application No. : 10/810,053
Filed : March 26, 2004
Title : DIGITAL TO ANALOG CONVERTER WITH REDUCED RINGING

Grp./Div. : 2819
Examiner : to be assigned

Docket No. : 51550/LTR/B600

INFORMATION DISCLOSURE STATEMENT
FOR A CONTINUING NON-CPA APPLICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
September 9, 2004

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, enclosed is FORM PTO/SB/08A/B.

The references listed thereon are not enclosed because they were of record in the parent application, Application No. 10/320,016, filed December 16, 2002. There is one additional reference that is listed on the form, namely, U.S. Patent No. 6,714,150. This is the patent which issued on the parent application (Application No. 10/320,016).

Application No. 10/810,053

It is respectfully requested that these references be considered in the examination of this application, and identified in the list of references cited on the patent issuing on this application.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By 

LeRoy T. Rahn
Reg. No. 20,356
626/795-9900

LTR/amb
Enclosure

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FORM PTO/SB/08A/B (10-01)
Substitute for PTO-1449A/B

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

Use as many sheets as necessary)

Attorney Docket Number

51550/LTR/B600

Application Number

10/810,053

Filing Date

March 26, 2004

Applicant(s)

Klaas Bult, et al.

Group Art Unit

2819

Examiner Name

To be assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	DOCUMENT NUMBER Number - Kind Code ² (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		4,905,189	02-1990	Brunolli
		5,105,193	04-1992	Vogt et al.
		5,689,257	11-1997	Mercer et al.
		5,825,317	10-1998	Anderson et al.
		5,870,044	02-1999	Dell'ova et al.
		5,920,273	07-1999	Hirano
		6,037,888	03-2000	Nairn
		6,052,074	04-2000	Iida
		US 6,191, 719 B1	02-20-2001	Bult et al.
		US 6,268,816 B1	07-31-2001	Bult et al.
		US 6,414,618 B2	07-02-2002	Bult et al.
		US 6,522,279 B2	02-18-2003	Bult et al.
		US 6,714,150 B2	03-30-204	Bult et al.

OTHER DOCUMENTS

EXAMINER INITIALS	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Article entitled "An 8-bit 2-ns Monolithic DAC", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, Feb. 1988, pp. 142-146
		Article entitled "a 27-MHz Digital-to-Analog Video Processor", IEEE Journal of Solid-State Circuits, Vol. 23, No. 6, Dec. 1988, pp. 1358-1369
		Vogt, Alexander W. et al., Article entitled "A 10-Bit High Speed CMOS DAC Macrocell", 1989, pp. 6.7.1-6.7.4

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EXAMINER SIGNATURE		DATE CONSIDERED	
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.pto.gov or MPEP 901.4. ³ Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English Language Translation is attached.			

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